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Sanganaka Pata

Newsletter

January 2020 – June 2020

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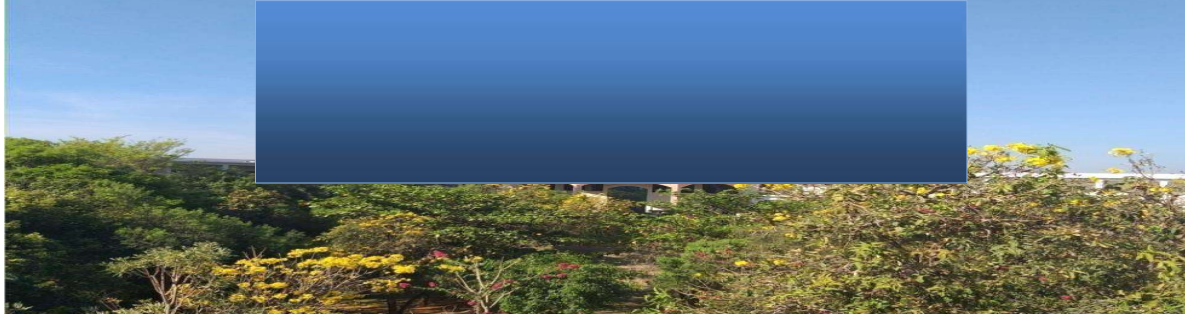
Department of Computer Science & Engineering

Don Bosco Institute of Technology

Mysore Road, Kumbalagodu, Bengaluru – 560 074



Sanganaka Pata



Vision

To become center of excellence for educating students in Computer Science and Engineering, and enabling them to become successful and socially responsible future software professionals.

Mission

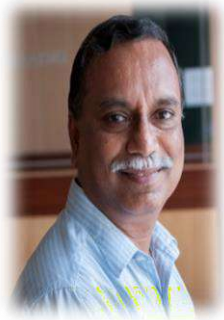
M1: To impart knowledge that builds software skills with logical and structured thinking.

M2: To provide both formal and informal learning environments to become globally competitive software professionals.

M3: To motivate towards becoming a quick learner, good team player, acquire leadership skills with excellent communication, time and task management.

M4: To inculcate professional ethics and concern for the environment for the benefits of society.





HOD's Message

Prof. B. S. Umashankar

Professor & Head

Department of Computer Science & Engineering

On behalf of Management, Principal, and Staff of Department of Computer Science & Engineering, with great happiness and pride, I place on record the memorable achievements of our staff and students during the even semester 2019-20.

Hearty congratulations to Dr. Manjunathswamy B. E., Associate Professor, Dept. of CSE for his proposal that received Rs. 6.70 lakhs grant from All India Council for Technical Education (AICTE) for conducting FDP on the topic Massive Parallel Super Computing using OpenCL Framework on the Heterogeneous Computing Platform during January 2020.

I thank the Management, Principal, Coordinators and organizing committee members for the successful conduction of AICTE sponsored 2-week Faculty Development Programme on “Massive Parallel Super Computing using OpenCL Framework on the Heterogeneous Computing Platform. It was inaugurated on January 6th, 2020 by Dr. Venugopal K. R., Hon. Vice Chancellor, Bangalore University, Bangalore.

Academic activities went online due to pandemic. I thank all the faculty members for their grit and determination to face the challenges and ensure that learning did not stop. Special thanks to students and their parents for their support.

To ensure rich learning experiences, several webinars were conducted for students and faculty members.

Faculty Achievements

Grants Received

- **Dr. Manjunathswamy B. E.** received Rs. 6.70 lakhs grant from All India Council for Technical Education (AICTE) for conducting FDP on the topic *Massive Parallel SuperComputing using OpenCL Framework on the Heterogeneous Computing Platform* during January 2020.

Publications

Session Chair / Guest Lectures

- **Bhaskar R.** presented Guest Lecture on "Research outlook on Cloud Computing" at SSIT Tumkur, on 24th January 2020.

Workshop/FDP Attended

- **Sheeba S.** and **Komala D.** attended Three days workshop on "*Microcontroller and Embedded Systems*" from 20th to 22nd January 2020 conducted by Dept. of Computer Science & Engg. at SJBIT, Bangalore and one day workshop on Outcome Based Education from 27th to 31st January 2020 conducted by Dept of Electronics and Communication Engg. at DBIT, Bangalore.
- **Giridhar Gowda** attended FDP on *National Cyber Safety and Security Standards* "on 1st March 2019 organized by National Cyber Safety and Security Standards at Kochi, Kerala, participated in *National Symposium on Technology and Innovations Interface with IPR* on 28th January 2020 held at Indian Institute of Science, Bangalore.
- **Faculty Members of CSE** attended AICTE sponsored Two Weeks Faculty Development Programme on *Massive Parallel Super Computing using OpenCL Framework on the Heterogeneous Computing Platform* from 6th to 18th January 2020 organized by Dept. of CSE, DBIT.

Faculty Development Programme

AICTE sponsored 2-week Faculty Development Programme on “Massive Parallel Super Computing using OpenCL Framework on the Heterogeneous Computing Platform” was inaugurated on January 6th, 2020 by Dr. Venugopal K. R., Hon. Vice Chancellor, Bangalore University, Bangalore.



Dr. Hemadri Naidu T., Principal, DBIT, Prof. Umashankar B. S., HOD-CSE, Dr. Manjunathswamy B. E., Coordinator welcoming the Chief Guest Dr. Venugopal K. R., Vice-Chancellor, Bangalore University, Bangalore.



Group Photo of the Delegates and Staff of DBIT with the Chief Guest

Technical Sessions



“Evolution of Parallel Computing and Architecture of Parallel Computing” by
Dr. K. V. Subramaniam, Professor,
PES University



“Introduction to High Performance Computing, Shared Memory Parallel Programming and Distributed Memory Parallel Computing” by Mr.
Ravi Teja, AMD India Pvt. Ltd.



“Heterogeneous Computing” by
Dr. Prakash Raghavendra,
Fellow, AMD India Pvt. Ltd.



Press Clipping Vijaya Vani
Kannada Newspaper
Bangalore Edition, 12-1-2020

Webinar

Webinar on “Issues and challenges in Cyber Security” by Dr. Gururaj H. L, ACM Distinguished speaker, Associate Professor, Dept of CSE, VVCE, Mysuru.

June 2th, 2020

The whole Webinar aimed at enhancing knowledge of the participants regarding basic on issues of cyber security. The objectives of the webinar are Increasing awareness about cybercrimes. The webinar was hosted on 2th June 2020 from 2.00 PM to 4.00 PM at DBIT Campus, Bengaluru Students and Staff from various Institutions across the country participated actively in the interactive session. The feedback received from the participants has been very encouraging and has motivated us to plan many more such webinars.

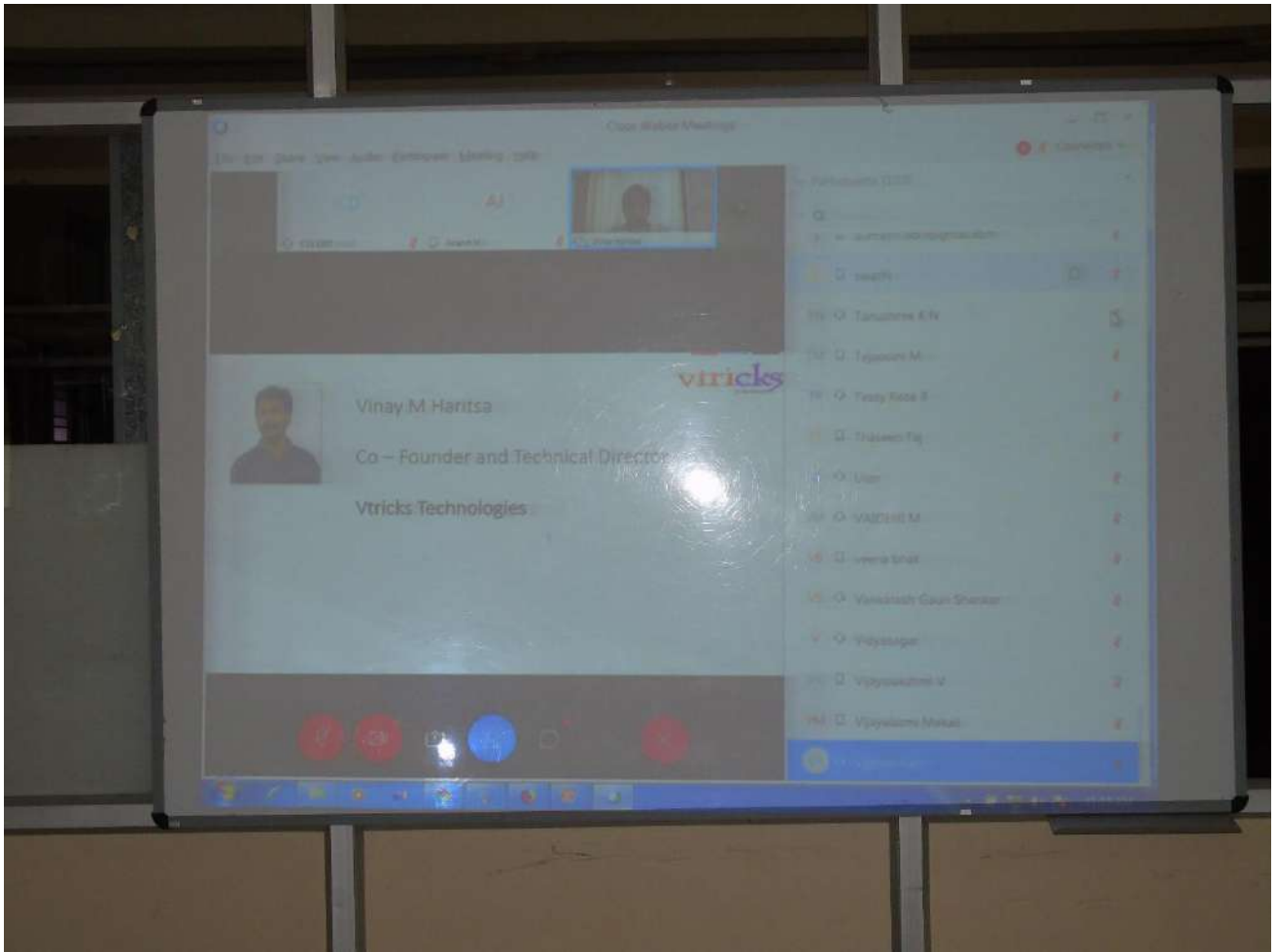


Welcome address by Dr. Hemadri Naidu T., Principal, DBIT

Webinar on “Data Science and Machine Learning- Project – Architecture and Functional Overview and Overview of High-Performance Computing” by Mr. Vinay Harista, Technical Director, Vtricks Technologies, Bengaluru and Dr. Prakash Raghavendra Fellow (Software Development) AMD India Pvt. Ltd, Bengaluru.

May 29th and 30th, 2020

The whole Webinar aimed at learning leading-edge HPC technologies and skills to exploit the full potential of the world’s largest supercomputers and multicore processors. This is a well-established programme that has been successful in training generations of specialists in parallel programming. High Performance Computing is a key area supporting most areas of scientific research and industry. On 29th May, Mr. Vinay M Haritsa delivered webinar on “Data Science and Machine Learning Project Architecture and functional overview” and on 30th May, Dr. Prakash Raghavendra delivered webinar on “Overview of High-Performance Computing”. The webinar was hosted on May 29th and 30th, 2020 from 10.30 AM to 12.00 PM at DBIT Campus, Bengaluru. Students and Staff from various Institutions across the country participated actively in the interactive session. The feedback received from the participants has been very encouraging and has motivated us to plan many more such webinars.



Overview of High-Performance Computing by Mr. Vinay M Haritsa

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CSE

Editorial Team

Chief Editor: Prof. B. S. Umashankar, HOD, Dept of CSE

Editorial Committee Members

Manjula B S, Assistant professor, Dept of CSE

