Programme:	**Electronics and Communication Engineering**	**Semester**	**8th**
Batch	**2019-2023**	**A.Y.**	**2022-23**
Course Title	**Technical Seminar**	**Course Code**	**18ECS84**
Evaluation Technique	**Oral presentation & Report**	**Evaluation identifier**	**Review 2**

EC identifier	EC description	Allotted marks	Percentage of Allotted marks				
			20%	20%	20%	20%	20%
EC7	Realization	10	Able to talk comprehensively about the problem space	Able to talk comprehensively about the solution space around the selected topic	Able to talk about the architectural features of the technology	Able to talk about the design and development of product based on the studied technology	Able to talk about the actual deployment of the solution based on the studied technology
EC8	Contemporaries	10	Able to talk about contemporary technologies	Able to talk about multitude of applications	Present contemporary technologies thro' additional research papers surveyed	Present contemporary problems being addressed thro' additional research papers surveyed	Able to talk about any products/deployments based on the technologies studied
EC9	Futuristic	10	Able to talk about the scope and limitations	Able to relate the studied technology solutions for societal concerns	Able to quantitatively evaluate the performance	Able to qualitatively present the performance	Highlight research pointers
EC4	Presentation	10	Aesthetics [Font combinations, Grammer, acknowledged sources, Originality (no copy/paste), neatness, color combination]	Illustrative Figures & visuals - Originals/copied from acknowledged sources + being able to explain	Elaborate beyond the slide content verbally	smooth transition of slides, topics, emphasizing, etc	Timeliness
EC10	Documentation	5	Completely as per the template	No fundamental grammatical errors including spellings	Documentation style (fonts, headers, type uniformity, etc.)	Originally created figures, charts & diagrams	Use of specialized tools for documentation like latex, equation editor, graphing tools etc.
EC6	Q & A	5	Able to answer binary questions to the point	Able to elaborate	Support with examples	Futuristic thoughts	Theoretical/research investigations

Coordinators:

HoD

Professor & H.O.D
Dept. of Electronics & Communication
DON BOSCO INSTITUTE OF TECHNOLOGY
Kumbalagodu, BANGALORE-560 074

Scheme of Evaluation

Programme:	**Electronics and Communication Engineering**	**Semester**	**8th**
Batch:	**2019-2023**	**A.Y.**	**2022-23**
Course Title:	**Technical Seminar**	**Course Code**	**18ECS84**
Evaluation Technique:	**Oral presentation**	**Evaluation identifier**	**Review 1**

EC identifier	EC description	Allotted marks	Percentage of Allotted marks				
			20%	20%	20%	20%	20%
EC1	Motivation	10	Mentions about people (including the guide) with whome he/she has interacted before selecting the topic.	Provides illustrations of the problems whose solution is addressed by the selected topic	Provides illustrations of the problems and the technology solutions thereof in the backdrop of the selected topic	Looks passionate about the selected topic	Explicit mention about motivation in the presentation slides
EC2	Literature Survey	10	Nominal websites	Less than 5 conference papers	5-7 conference papers	[1-3] jnl papers	[1-2] IEEE jnl papers
EC3	Understanding	10	Able to explain what the cocept is	able to provide the theoretical background	Quantitative framework	Build case studies	scope+limitations
EC4	Presentation	10	Aesthetics [Font combinations, Grammer, acknowledged sources, Originality (no copy/paste), neatness, color combination]	Illustrative Figures & visuals - Originals/copied from acknowledged sources + being able to explain	Elaborate beyond the slide content verbally	smooth transition of slides, topics, emphasizing, etc	Timeliness
EC5	Attitude & Behaviour	5	Greetings and complimentary closing of presentation	Outfit (well dressed and groomed)	Connect with the audience	Conviction	Body language
EC6	Q & A	5	Able to answer binary questions to the point	Able to elaborate	Support with examples	Futuristic thoughts	Theoretical/research investigations

Coordinators:

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DON BOSCO INSTITUTE OF TECHNOLOGY

KUMBALAGODU, BENGALURU

Department of Electronics and Communication Engineering

Technical Seminar Evaluation (18ECS84) Rubrics

Evaluation Identifier: Review I
Evaluation Technique: Oral Presentation
EC: Evaluation Criteria

EC1: Motivation
EC2: Literature Survey
EC3: Understanding
EC4: Presentation
EC5: Attitude & Behavior
EC6: Q&A

Evaluation Identifier: Review II
Evaluation Technique: Oral Presentation & Report

EC7: Realization
EC8: Contemporaries
EC9: Futuristic
EC4: Presentation
EC10: Documentation
EC6: Q&A

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8TH Sem TECHNICAL SEMINAR REVIEW-II SCHEDULE

Note: - To be Submitted:

- **Base Paper (to guide)**
- **Abstract (to seminar coordinator)**
- **PPT (to guide)**
- **Draft Report (to guide)**
- **Final Report (one hard copy to guide & second hard copy to Seminar coordinator (Spiral Binded Report).**
- **Review-II presentation includes advancement and improvement in same topic content of review – I with CO-PO (to be retained same as sample given) & CO-PSO (Varies as per topic scope) Mapping.**

Project Group No.	Project Associates	USN	Guide & Panel Members	Dates & Duration	
1	AFREEN TAJ	1DB19EC004	Dr. Sharanabasappa Mrs. Chaitra S. Mr. Kishore Kumar	04-05-23	9.10–9.30am
	BHARGAVI AGNIHOTRI	1DB19EC019			9.30-9.50am
	KAVYA K	1DB19EC058			9.50-10.10am
	KAVYA S HALLUR	1DB19EC062			10.10-10.30am
2	AISHWARYA K M	1DB19EC005	Mrs. Lakshmidevi T. R. Mrs. Tejaswini M. L. Mrs. Rashmi K. T.	04-05-23	10.30-10.50am
	AMRITHA PRIYA A	1DB19EC007			10.50-11.10am
	ARPITHA P	1DB19EC011			11.10-11.30am
	HEMASHREE R	1DB19EC051			11.30-11.50am
3	KEERTHAN R MATHAD	1DB19EC063	Mrs. Lakshmidevi T. R. Mrs. Tejaswini M. L. Mrs. Rashmi K. T.	04-05-23	11.50-12.10pm
	NAMITHA S R	1DB19EC082			12.10-12.30pm
	NAYANA M	1DB19EC086			12.30-12.50pm
	P P HAEMNATH	1DB19EC089			12.50-1.10pm
4	MALLANGOUDA S PATIL	1DB19ET001	Dr. Jai Prakash Prasad Mr. C. Y. Gopinath Mrs. Nandini A.S. Mr. Yogesh G	04-05-23	2.00-2.20pm
	PAVAN J	1DB19ET003			2.20-2.40pm
5	LAVANYA N	1DB19EC071	Mrs. Lakshmidevi T. R. Mrs. Tejaswini M. L. Mrs. Rashmi K. T.	04-05-23	2.40-3.00pm
	N M NAYANA	1DB19EC080			3.00-3.20pm
	NAMRATHA M	1DB19EC083			3.20-3.40pm
	POORNIMA S	1DB19EC090			3.40-4.00pm
6	DHARSHANIKA A N	1DB19EC040	Dr. Bhagya P. Mrs. Shwetha B. V. Mrs. Pavithra H. V.	04-05-23	9.10–9.30am
	DIVYA R VAIDYA	1DB19EC041			9.30-9.50am
	HARINI S M	1DB19EC046			9.50-10.10am
	HEERA N	1DB19EC050			10.10-10.30am
7	KISHAN P S	1DB19EC065	Mrs. Lakshmidevi T. R. Mrs. Tejaswini M. L. Mrs. Rashmi K. T.	04-05-23	10.30-10.50am
	MOHAMMED ATIQUE	1DB19EC077			10.50-11.10am
	NUTHAN Y J	1DB19EC087			11.10-11.30am

	PRAMODH H R	1DB19EC092			11.30-11.50am
8	KIRAN G M	1DB19EC064	Mrs. Roopa B. S. Mrs. Shubha G. N. Mrs. Swetha K	04-05-23	11.50-12.10pm
	MOHAN M	1DB19EC078			12.10-12.30pm
	NAMRATHA S	1DB19EC084			12.30-12.50pm
	NANDITHA H	1DB19EC085			12.50-1.10pm
9	BHARAT V G	1DB19EC016	Dr. Sharanabasappa Mrs. Chaitra S. Mr. Kishore Kumar	04-05-23	9.10–9.30am
	SANJAY GOWDA M D	1DB19EC112			9.30-9.50am
	SATHYA R	1DB19EC115			9.50-10.10am
	SUDHANVA N PRASAD	1DB19EC125			10.10-10.30am
10	AMRUTH S	1DB19EC008	Dr. Bhagya P. Mrs. Shwetha B. V. Mrs. Pavithra H. V.	04-05-23	10.30-10.50am
	HEBBARE SUMEET SHREEDHAR	1DB19EC049			10.50-11.10am
	JAYANTH SHUKLA	1DB19EC057			11.10-11.30am
	VIKAS M A	1DB19EC136			11.30-11.50am
11	ANANYA V	1DB19EC009	Dr. Bhagya P. Mrs. Shwetha B. V. Mrs. Pavithra H. V.	04-05-23	11.50-12.10pm
	CHANDANA N	1DB19EC030			12.10-12.30pm
	SOUNDARYA A M	1DB19EC122			12.30-12.50pm
12	CHIRANJEEVI C M	1DB19EC035	Mrs. Roopa B. S. Mrs. Shubha G. N. Mrs. Swetha K	04-05-23	9.10–9.30am
	JAYANTH C	1DB19EC056			9.30-9.50am
	GAANA S KUMAR	1DB19EC045			9.50-10.10am
	HARSHITA B	1DB19EC047			10.10-10.30am
13	AKASH R	1DB19EC006	Dr. Shashiranjan Mrs. Soujanya A. P. Mrs. Shwetha M.	04-05-23	10.30-10.50am
	BHASKAR M	1DB19EC020			10.50-11.10am
	DARSHAN P	1DB19EC036			11.10-11.30am
	DEEPAK L	1DB19EC038			11.30-11.50am
14	ARUN SUBAN M	1DB19EC012	Mrs. Roopa B. S. Mrs. Shubha G. N. Mrs. Swetha K	04-05-23	11.50-12.10pm
	CHETHAN M	1DB19EC031			12.10-12.30pm
	CHETHAN M R	1DB19EC032			12.30-12.50pm
	RAJESH KUMAR J	1DB19EC102			12.50-1.10pm
15	RASHMI S	1DB19EC105	Dr. A. N. Maheswarappa Dr. Chandrashekar N. S. Mr. Suresha H.S.	04-05-23	9.10–9.30am
	SAKSHI DAMODAR UMARJI	1DB19EC111			9.30-9.50am
	SHRUSTI SHETTY .S	1DB19EC119			9.50-10.10am
	SURAJ S	1DB19EC128			10.10-10.30am
16	SUSHMITHA R	1DB19EC129	Dr. Shashiranjan Mrs. Soujanya A. P. Mrs. Shwetha M.	04-05-23	10.30-10.50am
	VAIBHAV R	1DB19EC132			10.50-11.10am
	VAISHNAVI H N	1DB19EC133			11.10-11.30am
	YASHWANTH M P	1DB19EC143			11.30-11.50am

17	KISHORE V	1DB19EC066	Mrs. Babitha S. Mrs. Shruthi G. Mrs. Mahadevi S Manur	04-05-23	9.10–9.30am
	KUMUDHA L	1DB19EC067			9.30-9.50am
	PREMA B G	1DB19EC095			9.50-10.10am
	SRIHARI R	1DB19EC123			10.10-10.30am
18	KUSHAL GOWDA G	1DB19EC068	Mrs. Babitha S. Mrs. Shruthi G. Mrs. Mahadevi S Manur	04-05-23	10.30-10.50am
	THILAK K	1DB19EC131			10.50-11.10am
	VENKATACHALANAIDU R	1DB19EC134			11.10-11.30am
	SAKSHI SURESH JADHAV	1DB20EC400			11.30-11.50am
19	MOHAMMED ANIS	1DB19EC076	Dr. Jai Prakash Prasad Mr. C. Y. Gopinath Mrs. Nandini A.S. Mr. Yogesh G	04-05-23	11.50-12.10pm
	RANJAN R	1DB19EC104			12.10-12.30pm
	SANNIDHI H B	1DB19EC114			12.30-12.50pm
	YASHAS S	1DB19EC142			12.50-1.10pm
20	PRERANA JAIN	1DB19EC096	Dr. Jai Prakash Prasad Mr. C. Y. Gopinath Mrs. Nandini A.S. Mr. Yogesh G	04-05-23	9.10–9.30am
	RAHIL AHMED	1DB19EC100			9.30-9.50am
	VINAY C	1DB19EC137			9.50-10.10am
21	BHOOMIKA S	1DB19EC023	Mrs. Babitha S. Mrs. Shruthi G. Mrs. Mahadevi S Manur	03-05-23	9.10–9.30am
	BRUNDA R	1DB19EC024			9.30-9.50am
	KAVYA R	1DB19EC061			9.50-10.10am
22	LAVANYA GM	1DB19EC070	Dr. Sharanabasappa Mrs. Chaitra S. Mr. Kishore Kumar	03-05-23	9.10–9.30am
	RADHIKA S	1DB19EC099			9.30-9.50am
	SUMATI MS	1DB19EC126			9.50-10.10am
	SUPRIYA M	1DB19EC127			10.10-10.30am
23	REETHA N M	1DB19EC106	Dr. Bhagya P. Mrs. Shwetha B. V. Mrs. Pavithra H. V.	03-05-23	10.30-10.50am
	SAHANA S	1DB19EC110			10.50-11.10am
	SNEHA M	1DB19EC121			11.10-11.30am
	SHRUTHI S	1DB19EC120			11.30-11.50am
24	MONISHA R	1DB19EC079	Dr. Bhagya P. Mrs. Shwetha B. V. Mrs. Pavithra H. V.	03-05-23	9.10–9.30am
	PREETHI N	1DB19EC093			9.30-9.50am
	PRIYA V	1DB19EC097			9.50-10.10am
25	CHAITANYA S	1DB19EC026	Dr. A. N. Maheswarappa Dr. Chandrashekar N. S. Mr. Suresha H.S.	03-05-23	10.10-10.30am
	CHANDANA D	1DB19EC029			10.30-10.50am
	DIVYASHREE B	1DB19EC042			10.50-11.10am
26	MANOJ S R	1DB19EC073	Dr. A. N. Maheswarappa Dr. Chandrashekar N. S. Mr. Suresha H.S.	03-05-23	11.10-11.30am
	PRADYUMNYA K G	1DB19EC091			11.30-11.50am
	SANJITH KUMAR	1DB19EC113			11.50-12.10pm
	VIJETHA U M	1DB19EC135			12.10-12.30pm

27	MAMATHA BM	1DB19ET002	Dr. Jai Prakash Prasad Mr. C. Y. Gopinath Mrs. Nandini A.S. Mr. Yogesh G	03-05-23	12.30-12.50pm
	SHOBITHA P	1DB19ET004			12.50-1.10pm
	TEJASHWINI PATIL	1DB19ET005			9.10–9.30am
28	ANUJNA A	1DB19EC010	Mrs. Roopa B. S. Mrs. Shubha G. N. Mrs. Swetha K	03-05-23	9.30-9.50am
	BHAVANA	1DB19EC021			9.50-10.10am
	CHINMAYI R	1DB19EC033			10.10-10.30am
	HIMABINDU B C	1DB19EC052			10.30-10.50am
29	KUSUMA S JAIKANT	1DB19EC069	Dr. A. N. Maheswarappa Dr. Chandrashekar N. S. Mr. Suresha H.S.	03-05-23	10.50-11.10am
	NAGADEERAJ PS	1DB19EC081			11.10-11.30am
30	SUDEEP R	1DB19EC124	Dr. Sharanabasappa Mrs. Chaitra S. Mr. Kishore Kumar	03-05-23	11.30-11.50am
	SYED IKRAM	1DB19EC130			11.50-12.10pm
	YASHAS H S	1DB19EC140			12.10-12.30pm
	YASHAS R V	1DB19EC141			12.30-12.50pm
31	ABH[illegible]PATIL	1DB19EC001	Mrs. Babitha S. Mrs. Shruthi G. Mrs. Mahadevi S Manur	02-05-23	12.50-1.10pm
	BG TEJAS	1DB19EC013			9.10–9.30am
	BS JAYANTH	1DB19EC014			9.30-9.50am
	C SHREYAS	1DB19EC025			9.50-10.10am
32	PRERSHA [illegible]JAIN	1DB19EC094	Dr. A. N. Maheswarappa Dr. Chandrashekar N. S. Mr. Suresha H.S.	02-05-23	4.00-4.20pm
	PRIYANKA RY	1DB19EC098			4.20-4.40pm
33	ABHISHEK N	1DB19EC002	Dr. Jai Prakash Prasad Mr. C. Y. Gopinath Mrs. Nandini A.S. Mr. Yogesh G	02-05-23	9.10–9.30am
	BHARATHESH K	1DB19EC017			9.30-9.50am
	K MANJU[illegible]TH	1DB19EC059			9.50-10.10am
	CHANDA[illegible]	1DB19EC028			10.10-10.30am
34	B S RAKSH[illegible]HA	1DB19EC015	Dr. Sharanabasappa Mrs. Chaitra S. Mr. Kishore Kumar	02-05-23	10.30-10.50am
	CHAITHR[illegible]H	1DB19EC027			10.50-11.10am
	DEEKSHIT[illegible]M	1DB19EC037			11.10-11.30am
	BHARATH[illegible]	1DB19EC018			11.30-11.50am
35	ABHISHE[illegible]	1DB19EC003	Dr. Bhagya P. Mrs. Shwetha B. V. Mrs. Pavithra H. V.	02-05-23	11.50-12.10pm
	BHAVANA[illegible]	1DB19EC022			12.10-12.30pm
	CHIRAG M	1DB19EC034			12.30-12.50pm
	JAGDISH L	1DB19EC055			12.50-1.10pm
36	J TARUN	1DB19EC054	Dr. Shashiranjan Mrs. Soujanya A. P. Mrs. Shwetha M.	02-05-23	9.10–9.30am
	MADHUKUMAR P N	1DB19EC072			9.30-9.50am
	RAHUL CHANDRASHEKAR	1DB19EC101			9.50-10.10am
	SHASHIDHARA D	1DB19EC116			10.10-10.30am

37	DHANUSH M	1DB19EC0039	Mrs. Babitha S. Mrs. Shruthi G. Mrs. Mahadevi S Manur	02-05-23	10.30-10.50am
	DIVYASHREE N	1DB19EC043			10.50-11.10am
	HARSHITHA P	1DB19EC048			11.10-11.30am
38	RUCHITHA V A	1DB19EC108	Mrs. Lakshmidevi T. R. Mrs. Tejaswini M. L. Mrs. Rashmi K. T.	02-05-23	11.30-11.50am
	SHILPA LAMANI	1DB19EC117			11.50-12.10pm
	SHILPA Y	1DB19EC118			12.10-12.30pm
	SHASHANK N	1DB20EC402			12.30-12.50pm
39	ISHA SHANKAR G	1DB19EC053	Mrs. Roopa B. S. Mrs. Shubha G. N. Mrs. Swetha K	02-05-23	12.50-1.10pm
	KAMASANI UHA	1DB19EC060			9.10–9.30am
	S M VAISHNAVI	1DB19EC109			9.30-9.50am
	RAKSHITH R	1DB19EC103			9.50-10.10am
40	OMKARI PADMANABHA	1DB19EC088	Mr. Shashiranjan Mrs. Soujanya A. P. Mrs. Shwetha M.	02-05-23	10.10-10.30am
	WILSON N	1DB19EC139			10.30-10.50am
	PAVITHRA E	1DB20EC401			10.50-11.10am
	VISHNU VIJAY A N	1DB20EC403			11.10-11.30am
41	ABHISHEK GOWDA V	1DB18EC002	Mr. Shashiranjan Mrs. Soujanya A. P. Mrs. Shwetha M.	02-05-23	11.30-11.50am
	KRUTHIK M V	1DB18EC061			11.50-12.10pm
	NANDEESH A R	1DB18EC080			12.10-12.30pm
	PRANAV G	1DB18EC096			12.30-12.50pm
42	YASHU PRAJWAL NAG D	1DB17TE030	Dr. Shashiranjan Mrs. Soujanya A. P. Mrs. Shwetha M.	02-05-23	2.00-2.20pm
	RUTWIK R HEGDE	1DB18TE011			2.20-2.40pm

Coordinators:

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Department of Electronics and Communication Engineering

Evaluation Sheet

Progamme	**B.E.(Electronics and Communication Engineering)**	Semester	**8th**
Batch	**2019-23**	A.Y.	**2022-23**
Course Title	**Technical Seminar**	Course code	**18ECS84**
Eval. Technique	**Oral Presentation**	Eval. Identifier	**Review 1**

Note- EC: Evaluation Criteria, **EC1**: Motivation, **EC2**: Literature Survey, **EC3**: Understanding, **EC4**: Presentation, **EC5**: Attitude & Behavior, **EC6**: Q&A.

Project Guide Name: Dr. Jai Prakash Prasad

Project Batch No	USN	Student Name	Technical Seminar Title	EC1 (10)			EC2 (10)			EC3 (10)			EC4 (10)			EC5 (5)			EC6 (5)			Total (50)
				1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	
4	1DB19ET001	MALLANGOUDA S PATIL	An improved deep network based scene classification method for self-driving cars	10	10	10	10	10	10	9	9	9	9	9	9	5	5	5	4	4	4	47
	1DB19ET003	PAVAN J	The implication of neuralink and Brain machine Interface Technologies	10	10	10	10	10	10	9	9	9	9	9	9	5	5	5	4	4	4	47
20	1DB19EC096	PRERANA JAIN	Enhanced smart waste management system with IoT integrated cloud	9	10	10	9	9	9	9	10	9	10	10	10	4	4	4	4	4	4	46
	1DB19EC100	RAHIL AHMED	AI in waste Electronic and Electrical Equipment Treatment.	10	9	9	10	9	9	9	10	9	10	10	10	4	4	4	4	4	4	46
	1DB19EC137	VINAY C	Role of wireless wearable Technologies in Recent Advancements	10	10	9	10	10	10	10	9	9	9	9	9	5	5	5	5	4	4	46

Examiners name with Signature:

1. Jai Prakash
2.
3.

Check Lists (to be submitted)

Base Paper (to guide)
Abstract (to coordinator)
PPT (to guide)
Draft Report (to guide)
Final Report (one copy to guide & second copy to coordinator)

Coordinator:

HoD:

Professor & H.O.D.
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Department of Electronics and Communication Engineering

Evaluation Sheet

Progamme	B.E.(Electronics and Communication Engineering)	Semester	8th
Batch	2019-23	A.Y.	2022-23
Course Title	Technical Seminar	Course code	18ECS84
Eval. Technique	Report	Eval. Identifier	Review 2

Note- EC: Evaluation Criteria, EC7: Realization, EC8: Contemporaries, EC9: Futuristic, EC4: Presentation, EC10: Documentation, EC6: Q&A.

Project Guide Name: Dr. Jai Prakash Prasad

Project Batch No	USN	Student Name	Technical Seminar Title	EC7 (10)			EC8 (10)			EC9 (5)			EC4 (10)			EC10 (10)			EC6 (5)			Total (50)
				1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	
4	1DB19ET001	MALLANGOUDA S PATIL	An improved deep network based scene classification method for self-driving cars	10	10	10	10	10	10	4	4	4	10	10	10	10	10	10	4	4	4	47
	1DB19ET003	PAVAN J	The implication of neuralink and brain machine Interface Technologies	10	10	10	10	10	10	4	4	4	10	10	10	10	10	10	4	4	4	47
20	1DB19EC096	PRERANA JAIN	Enhanced smart waste management system with Incinerator composter	9	9	9	9	9	9	4	4	4	10	10	10	10	10	10	4	4	4	46
	1DB19EC100	RAHIL AHMED	AI in waste Electronic and Electrical equipment Treatment	9	9	9	9	9	9	4	4	4	10	10	10	10	10	10	4	4	4	46
	1DB19EC137	VINAY C	Role of wireless wearable Technologies in Recent Advancements	9	9	9	9	9	9	4	4	4	10	10	10	10	10	10	4	4	4	46

Examiners name with Signature:

1. Jai Prakash
2.
3.

Check Lists (to be submitted)

Base Paper (to guide)
Abstract (to coordinator)
PPT (to guide)
Draft Report (to guide)
Final Report (one copy to guide & second copy to coordinator)

Coordinator:

HoD:

Professor & H.O.D.
Dept. of Electronics & Communication
DON BOSCO INSTITUTE OF TECHNOLOGY
Kumbalagodu, BANGALORE-560 074

Wayanamac Education Trust ®

Don Bosco Institute of Technology, Bangalore

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Ph.: +91- 80-28437028 / 29 /30 Fax: +91 -80-28437031
Website: www.dbit.co.in Email: ece@dbit.co.in

Department of Electronics and Communication Engineering

Evaluation Sheet

Progamme	**B.E.(Electronics and Communication Engineering)**	Semester	**8th**
Batch	**2019-23**	A.Y.	**2022-23**
Course Title	**Technical Seminar**	Course code	**18ECS84**
Eval. Technique	**Oral Presentation**	Eval. Identifier	**Review 1**

Note- EC: Evaluation Criteria, **EC1**: Motivation, **EC2**: Literature Survey, **EC3**: Understanding, **EC4**: Presentation, **EC5**: Attitude & Behavior, **EC6**: Q&A.

Project Guide Name: Mrs. Shubha G. N.

Project Batch No	USN	Student Name	Technical Seminar Title	EC1 (10)			EC2 (10)			EC3 (10)			EC4 (10)			EC5 (5)			EC6 (5)			Total (50)
				1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	
12	1DB19EC035	CHIRANJEEVI C M	Slot loading effect on the impedance and radiation performance of the TM03 mode high gain square patch antenna	10	10	10	10	10	10	9	9	9	9	9	9	5	5	5	5	5	5	48
	1DB19EC056	JAYANTH C	A simple model to compute the characteristic parameters of a slotted rectangular patch antenna	10	10	10	10	10	10	9	9	9	9	9	9	5	5	5	5	5	5	48
	1DB19EC045	GAANA S KUMAR	Impact of triangular - rectangular slots in the Patch and Partial ground plane on Rectangular patch antenna	10	10	10	10	10	10	9	9	9	9	9	9	5	5	5	4	4	4	47
	1DB19EC047	HARSHITA B	A compact high gain multiband bowtie slot antenna with miniaturized triangular shaped MSP	10	10	10	10	10	10	9	9	9	9	9	9	5	5	5	4	4	4	47

Examiners name with Signature:

1. Shubha G.N
2. Roopa. B.S
3. Swetha. K

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Course Title	**Technical Seminar**	Course code	**18ECS84**
Eval. Technique	**Report**	Eval. Identifier	**Review 2**

Note- EC: Evaluation Criteria, EC7: Realization, EC8: Contemporaries, EC9: Futuristic, EC4: Presentation, EC10: Documentation, EC6: Q&A.

Project Guide Name: Mrs. Shubha G. N.

Project Batch No	USN	Student Name	Technical Seminar Title	EC7 (10)			EC8 (10)			EC9 (5)			EC4 (10)			EC10 (10)			EC6 (5)			Total (50)
				1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	
12	1DB19EC035	CHIRANJEEVI C M	Slot loading effect on the impedance and radiation performance of the TM03 mode high gain square patch antenna	10	10	10	10	10	10	4	4	4	9	9	9	10	10	10	5	5	5	48
	1DB19EC056	JAYANTH C	A simple model to compute the characteristic parameters of a slotted rectangular patch antenna	10	10	10	10	10	10	4	4	4	9	9	9	10	10	10	5	5	5	48
	1DB19EC045	GAANA S KUMAR	Impact of triangular rectangular slots in the patch and partial ground plane on rectangular patch antenna	10	10	10	10	10	10	4	4	4	10	10	10	10	10	10	4	4	4	47
	1DB19EC047	HARSHITA B	A compact high gain multiband bowtie slot antenna with metamaterial triangular shaped unit	10	10	10	10	10	10	4	4	4	10	10	10	10	10	10	4	4	4	47

Examiners name with Signature:

1. Shubha.G.N –
2. Roopa.B.S –
3. Swetha.IC

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Note- EC: Evaluation Criteria, **EC1**: Motivation, **EC2**: Literature Survey, **EC3**: Understanding, **EC4**: Presentation, **EC5**: Attitude & Behavior, **EC6**: Q&A.

Project Guide Name: Mrs. Lakshmidevi T. R.

Project Batch No	USN	Student Name	Technical Seminar Title	EC1 (10)			EC2 (10)			EC3 (10)			EC4 (10)			EC5 (5)			EC6 (5)			Total (50)
				1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	
7	1DB19EC065	KISHAN P S	IoT and ML Model of plant Disease prediction - Blisten Blight for Tea Plant	9	9	9	9	9	9	9	9	9	9	9	9	5	5	5	4	4	4	45
	1DB19EC077	MOHAMMED ATIQUE	Design and Implementation of 6-bit SAR ADC using 90nM CMOS processes.	10	10	10	10	10	10	10	10	10	10	10	10	5	5	5	5	5	5	50
	1DB19EC087	NUTHAN Y J	Battery Free Smart Bandage based on NFC RFID Technology	10	10	10	10	10	10	10	10	10	10	10	10	5	5	5	5	5	5	50
	1DB19EC092	PRAMODH H R	Implementation and Performance analysis of 8:1 multiplexer using different logic families of 180nm Technology	10	10	10	10	10	10	10	10	10	10	10	10	5	5	5	5	5	5	50
	1DB19EC108	RUCHITHA V A	IOT Based Greenhouse Agriculture	9	9	9	10	10	10	10	10	10	10	10	10	5	5	5	4	4	4	48

38	1DB19EC117	SHILPA LAMANI	Wheel Chair Controller using eye ball movement	9	9	9	10	10	10	10	10	10	10	10	10	10	15	15	15	4	4	4	48
	1DB19EC118	SHILPA Y	IoT - Based biometric recognition System in education	9	9	9	10	10	10	10	10	10	10	10	10	10	15	15	15	4	4	4	48
	1DB20EC402	SHASHANK N	Identification of fruit free path with Deep learning on Embedded drone to Achieve Accurate picked	9	9	9	9	9	9	9	9	9	9	9	5	5	8	4	4	4	45		

Examiners name with Signature:

1. TEJASWINI.M.L
2. LAKSHMIDEVI T R
3. RASHMI K T

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Abstract (to coordinator)
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Department of Electronics and Communication Engineering

Evaluation Sheet

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Batch	**2019-23**	A.Y.	**2022-23**
Course Title	**Technical Seminar**	Course code	**18ECS84**
Eval. Technique	**Report**	Eval. Identifier	**Review 2**

Note- EC: Evaluation Criteria, EC7: Realization, EC8: Contemporaries, EC9: Futuristic, EC4: Presentation, EC10: Documentation, EC6: Q&A.

Project Guide Name: Mrs. Lakshmidevi T. R.

Project Batch No	USN	Student Name	Technical Seminar Title	EC7 (10)			EC8 (10)			EC9 (5)			EC4 (10)			EC10 (10)			EC6 (5)			Total (50)
				1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	
7	1DB19EC065	KISHAN P S	IOT and ML Model of Plant Disease Prediction - Blister Blight for Tea Plant	9	9	9	9	9	9	4	4	4	9	9	9	10	10	10	4	4	4	45
	1DB19EC077	MOHAMMED ATIQUE	Design and implementation of 6-bit SAR ADC using 90nm CMOS processor.	10	10	10	10	10	10	5	5	5	10	10	10	10	10	10	5	5	5	50
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	1DB19EC092	PRAMODH H R	Implementation and Performance analysis of 8-bit ALU using different Logic families at 130nm Technology	10	10	10	10	10	10	5	5	5	10	10	10	10	10	10	5	5	5	50
	1DB19EC108	RUCHITHA V A	IOT Based Greenhouse Agriculture	9	9	9	10	10	10	5	5	5	10	10	10	10	10	10	4	4	4	48

38	1DB19EC117	SHILPA LAMANI	Wheel chair Controlled using eye ball movement.	9	9	9	10	10	10	5	5	5	10	10	10	10	10	10	4	4	4	48
	1DB19EC118	SHILPA Y	IoT Based Biometric Recognition System in education	9	9	9	10	10	10	5	5	5	10	10	10	10	10	10	4	4	4	48
	1DB20EC402	SHASHANK N	Identification of Fruit Tree Pests With Deep learning on Embedded Drone to Acheive Accurate Pesticide	9	9	9	9	9	9	5	5	5	10	10	10	9	9	9	4	4	4	45

Examiners name with Signature:

1. TEJASWINI.M.L
2. LAKSHMIDEVI T.E
3. RASHMI. K.T

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Dept. of ...
Nambalagodu, BANGALORE-560 074

WAYANAMAC EDUCATION TRUST(R)

DON BOSCO INSTITUTE OF TECHNOLOGY

Approved by AICTE New Delhi | Accredited by NBA New Delhi | Accredited by NAAC Bangalore | Affiliated to VTU Belagavi | Recognised by the Government of Karnataka

Programme:	Electronics and Communication Engineering	Semester:	8th
Batch:	2019-2023	A.Y.:	2022-23
Course Title:	Technical Seminar	Course Code:	18ECS84

OUTLINES FOR TECHNICAL SEMINAR PRESENTATION

- DATE CONFIRMATION CIRCULAR
- TECHNICAL SEMINAR PRESENTATION SCHEDULE (REVIEW-1)
- GUIDELINES FOR THE PREPARATION OF TECHNICAL SEMINAR REPORTS
- SCHEME OF EVALUATION (REVIEW 1 & 2)
- COURSE OUTCOMES (COs)
- TITLE, ABSTRACT AND REPORT HARD COPY SUBMISSION STATUS
- EXPECTED PPT SLIDES
- EXPECTED SEMINAR REPORT CONTENTS
- INSTITUTE & DEPT. VISION, MISSION, PEO, PO & PSO
- SEMINAR COURSE OUTCOMES (COs)
- COs-POs AND COs-PSOs MAPPING

By: Dr. Jai Prakash Prasad, Technical Seminar Coordinator, ECE Dept.

Don Bosco Institute of Technology, Bangalore

(NAAC Accredited Institution)

Department of Electronics and CommunicationEngineering

(Accredited by NBA & Permanently affiliated to VTU)

Date: 23-03-2023

CIRCULAR

8th SEM TECHNICAL SEMINAR (18ECS84)

It is hereby informed to all 8th sem students that the Technical Seminar Review-1 (Oral Presentation & Report) is scheduled from 04th April to 06th April 2023. All students must adhere to the schedule and Technical seminar guidelines. You are advised to consult your concerned project guide in advance one week before the scheduled date (for seminar presentation PPT, report, COs-POs and COs-PSOs Mapping).

Seminar Co-coordinator's

1. Dr. Jai Prakash Prasad
2. Prof. Gopinath C Y

23/03/23

HOD (ECE)

Professor & H.O.D.
Dept. of Electronics & Communication
DON BOSCO INSTITUTE OF TECHNOLOGY
Kumbalagodu, BANGALORE-560 074

8TH Sem TECHNICAL SEMINAR REVIEW-1 SCHEDULE

Reference OR Bibliography: The references should be **numbered serially** in the order of their occurrence in the text and their numbers should be indicated within square brackets for e.g. [3]. The section on references should list them in serial order in the following format.

1. For textbooks - A.V. Oppenheim and R.W. Schafer, Digital Signal Processing, Englewood, N.J., Prentice Hall, 3 Edition, 1975.
2. For papers - Devid, Insulation design to combat pollution problem, Proc of IEEE, PAS, Vol 71, Aug 1981, pp 1901-1907.

- Only SI units are to be used in the report. Important equations must be numbered in decimal form for e.g.
- **V = IZ** ..... **(3.2)**
- All equation numbers should be right justified.
- The seminar report should be brief and include descriptions of work carried out by others only to the minimum extent necessary. Verbatim reproduction of material available elsewhere should be strictly avoided. Where short excerpts from published work are desired to be included, they should be within quotation marks appropriately referenced.
- Proper attention is to be paid not only to the technical contents but also to the organization of the report and clarity of the expression. Due care should be taken to avoid spelling and typing errors. The student should note that report-write-up forms the important component in the overall evaluation of the project
- The reports submitted to the department/guide(s) must be spiral bounded, with a plastic covering.
- Separator sheets, used if any, between chapters, should be of thin paper.

23/03/23

23/3/23
Professor & H.O.D
Dept. of Electronics & Communication
DON BOSCO INSTITUTE OF TECHNOLOGY
Kumbalagodu, BANGALORE-560 074

NAME OF THE INSTITUTION
Address with pin code

1	Electronics & Communication (EC)	PURPLE

EVALUATION CRITERIA AND MARKS APPORTIONMENT

Programme:		Semester:		Remarks
Course Title:		Course Code:		
Assessment Criteria for the Reviews				
Review 1		**Review 2**		
Criterion	**Maximum Marks**	**Criterion**	**Maximum Marks**	
Motivation	10	Realization	10	
Literature survey	10	Contemporaries	10	
Understanding	10	Futuristic	10	
Presentation	10	Presentation	10	
Attitude & Behaviour	5	Documentation	5	
Q & A	5	Q & A	5	

SCHEME OF EVALUATION (REVIEW-1)			
Programme:	**Electronics and Communication Engineering**	**Semester**	**8th**
Batch:	**2019-2023**	**A.Y.**	**2022-23**
Course Title:	**Technical Seminar**	***Course Code***	***18ECS84***

Evaluation Technique:		Oral presentation				Evaluation identifier	Review 1
EC identifier	EC description	Allotted marks	Percentage of Allotted marks				
			20%	20%	20%	20%	20%
EC1	Motivation	10	Mentions about people (including the guide) with whome he/she has interacted before selecting the topic.	Provides illustrations of the problems whose solution is addressed by the selected topic	Provides illustrations of the problems and the technology solutions thereof in the backdrop of the selected topic	Looks passionate about the selected topic	Explicit mention about motivation in the presentation slides
EC2	Literature Survey	10	Nominal websites	Less than 5 conference papers	5-7 conference papers	[1-3] jnl papers	[1-2] IEEE jnl papers
EC3	Understanding	10	Able to explain what the cocept is	able to provide the theoretical background	Quantitative framework	Build case studies	scope+limitations
EC4	Presentation	10	Aesthetics [Font combinations, Grammer, acknowledged sources, Originality (no copy/paste), neatness, color combination]	Illustrative Figures & visuals - Originals/copied from acknowledged sources + being able to explain	Elaborate beyond the slide content verbally	smooth transition of slides, topics, emphasizing, etc	Timeliness
EC5	Attitude & Behaviour	5	Greetings and complimentary closing of presentation	Outfit (well dressed and groomed)	Connect with the audience	Conviction	Body language
EC6	Q & A	5	Able to answer binary questions to the point	Able to elaborate	Support with examples	Futuristic thoughts	Theoretical/research investigations

23/03/23

23/3/23
Professor & H.O.D.
Dept. of Electronics & Communication
BON BOSCO INSTITUTE OF TECHNOLOGY
Kumbalagodu

SCHEME OF EVALUATION (REVIEW-2)

Programme:	Electronics and Communication Engineering	Semester	8th
Batch	2019-2023	A.Y.	2022-23

Course Title	Technical Seminar				Course Code	18ECS84
Evaluation Technique	Oral presentation & Report				Evaluation identifier	Review 2
EC identifier	EC description	Allotted marks	Percentage of Allotted marks				
			20%	20%	20%	20%	20%
EC7	Realization	10	Able to talk comprehensively about the problem space	Able to talk comprehensively about the solution space around the selected topic	Able to talk about the architectural features of the technology	Able to talk about the design and development of product based on the studied technology	Able to talk about the actual deployment of the solution based on the studied technology
EC8	Contemporaries	10	Able to talk about contemporary technologies	Able to talk about multitude of applications	Present contemporary technologies thro' additional research papers surveyed	Present contemporary problems being addressed thro' additional research papers surveyed	Able to talk about any products/deployments based on the technologies studied
EC9	Futuristic	10	Able to talk about the scope and limitations	Able to relate the studied technology solutions for societal concerns	Able to quantitatively evaluate the performance	Able to qualitatively present the performance	Highlight research pointers
EC4	Presentation	10	Aesthetics [Font combinations, Grammer, acknowledged sources, Originality (no copy/paste), neatness, color combination]	Illustrative Figures & visuals - Originals/copied from acknowledged sources + being able to explain	Elaborate beyond the slide content verbally	smooth transition of slides, topics, emphasizing, etc	Timeliness
EC5	Documentation	5	Completely as per the template	No fundamental grammatical errors including spellings	Documentation style (fonts, headers, type uniformity, etc.)	Originally created figures, charts & diagrams	Use of specialized tools for documentation like latex, equation editor, graphing tools etc.
EC6	Q & A	5	Able to answer binary questions to the point	Able to elaborate	Support with examples	Futuristic thoughts	Theoretical/research investigations

23/08/23

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Kumbalagodu, BANGALORE-560 0

COURSE OUTCOMES (CO's)

Course: Technical Seminar **A.Y: 2022-23 Even**

				Assessment	

Course	Code	Credits	L-T-P	CIE	SEE	Exam Duration
B.E	18ECS84	1	0-0-2	100	-	3Hrs

Name of the Course coordinator: Dr. Jai Prakash Prasad, Prof. Gopinath C. Y.

Designation: Professor

CO1: Ability to identify state-of-the-art and futuristic technologies through self-motivation and through collaboration with others.

CO2: Ability to conduct a detailed literature survey and self-study in order to completely understand the intricacies of the chosen topic

CO3: Analysis and comprehension of proof-of-concept and related data.

CO4: Effective presentation and improve soft skills.

CO5: Make use of new and recent technology (e.g. Latex) for creating technical reports

COs-POs Mapping (Sample)

COs \ POs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2	1		2		1					2
CO2	2	3										2
CO3	2	2		2	1		1					2
CO4									3	3		1
CO5										3		1

COs-PSOs Mapping (Sample)			
PSOs / Cos	PSO1	PSO2	PSO3
CO1	3	3	3
CO2	3	1	
CO3	3	3	3
CO4			
CO5			3

23/3/23

TITLE, ABSTRACT AND REPORT SUBMISSION STATUS

COURSE OUTCOMES (CO's)

CO1: Ability to identify state-of-the-art and futuristic technologies through self-motivation and through collaboration with others.

CO2: Ability to conduct a detailed literature survey and self-study in order to completely understand the intricacies of the chosen topic.

CO3: Analysis and comprehension of proof-of-concept and related data.

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20/3/23

23/3/23

23/3/23

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VISVESVARAYA TECHNOLOGICAL UNIVERSITY

"Jnana Sangama", Belagavi-590018, Karnataka

A TECHNICAL SEMINAR REPORT

ON

"FUZZY FIRE CONTROL: UNLOCKING THE FUTURE OF AUTO SAFETY"

Submitted in partial fulfillment of the requirements for the award of the degree

BACHELOR OF ENGINEERING
IN
ELECTRONICS AND COMMUNICATION ENGINEERING

SUBMITTED BY

Ms. KUSUMA S JAIKANT

1DB19EC069

UNDER THE GUIDANCE OF

Prof. SURESHA H S

Associate professor

Dept of ECE, DBIT

Department of Electronics and Communication Engineering

DON BOSCO INSTITUTE OF TECHNOLOGY

Kumbalagodu, Mysore road, Bangalore-74

DON BOSCO INSTITUTE OF TECHNOLOGY

Kumbalagodu, Mysore Road, Bangalore-560074

Department of Electronics and Communication Engineering

CERTIFICATE

Certified that the Technical Seminar entitled **"Fuzzy Fire Control: Unlocking the feature of auto safety"** is carried out by **Ms. KUSUMA S JAIKANT** bearing **USN: 1DB19EC069**, a bonafide student of Don Bosco Institute of Technology, Bangalore in fulfillment for the award of degree of Bachelor of Engineering in Electronics and Communication Engineering of Visvesvaraya Technological University, Belgaum in the academic year 2022-2023. It is certified that all corrections/suggestions indicated during internal assessment have been incorporated in the report. The seminar report has been approved as it satisfies the academic requirement in respect of Technical Seminar work prescribed for the said degree.

Signature of the Guide	**Signature of Co-ordinator(s)**	**Signature of the HOD**
Prof. Suresha H S	**Prof. Jai Prakash Prasad**	**Dr. Maheswarappa A N**
Associate Professor	**Prof. Gopinath C Y**	Professor & HOD
Dept. of ECE	Associate Professors	Dept. of ECE
DBIT, Bangalore-74	Dept. of ECE	DBIT, Bangalore-74
	DBIT, Bangalore-74	

Professor & H.O.D.
Dept. of Electronics & Communication Engg.
DON BOSCO INSTITUTE OF TECHNOLOGY
Kumbalagodu, BANGALORE-560 074

Name of the Examiner	Signature of the Examiner
1) Dr. Maheswarappa A.N	1)
2) Prof. Suresha. H.S	2)

ABSTRACT

During automobile fire accidents, the lives couldn't be saved because of the failure in opening the vehicle doors. There are systems available for the detection and control of fire accidents in automobiles but those systems are designed to detect the fire, warn the drivers and operate the fire extinguishers to control the fire. The system designed in this research work tends to unlock the vehicle door automatically along with alerting the driver with an alarm and supply fire extinguishers during automobile fire. Temperature sensors and smoke sensors are used to detect the fire and different motors are used to open the door and supply the fire extinguisher. The system works even if the battery fails or the electrical system fails or the door locked manually. Since the separate system is installed for each door, in case if the system of a particular door fails, the passenger can be saved by the other door.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

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A TECHNICAL SEMINAR REPORT

ON

"The Evolution of Vehicle Safety Intelligence"

Submitted in partial fulfillment of the requirements for the award of the degree

BACHELOR OF ENGINEERING
IN
ELECTRONICS AND COMMUNICATION ENGINEERING

SUBMITTED BY

Mr. NAGADEERAJ P S

1DB19EC081

UNDER THE GUIDANCE OF

Prof. SURESHA H S

Associate professor

Dept of ECE, DBIT

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DON BOSCO INSTITUTE OF TECHNOLOGY

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Department of Electronics and Communication Engineering

CERTIFICATE

Certified that the Technical Seminar entitled **"The Evolution of Vehicle Safety Intelligence"** is carried out by **Mr. NAGADEERAJ P S** bearing **USN: 1DB19EC081**, a bonafide student of Don Bosco Institute of Technology, Bangalore in fulfillment for the award of degree of Bachelor of Engineering in Electronics and Communication Engineering of Visvesvaraya Technological University, Belgaum in the academic year 2022-2023. It is certified that all corrections/suggestions indicated during internal assessment have been incorporated in the report. The seminar report has been approved as it satisfies the academic requirement in respect of Technical Seminar work prescribed for the said degree.

Signature of the Guide	Signature of Co-ordinator(s)	Signature of the HOD
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ABSTRACT

This Seminar Paper proposes a safety management and control system for automotive anti-collision electronic fences combined with a safe distance-time model. This paper takes microcontroller as the core, and uses laser sensor, fingerprint recognition, and configuration display as the auxiliary research and development to design a visual anticollision safety protection electronic fence for automobiles. The possibility of collision is judged by the interaction of the location information of the positioning tag worn by the personnel and the virtual electronic fence of the mechanical equipment. This enables early warning. The experimental study shows that the safety management and control system of the anti-collision electronic fence for automobiles proposed in this paper does not need to change the existing structures of various vehicles. The system is convenient and quick to use, and also conforms to the habits of all users. The cost is low, the anti-collision effect is good, and it is easy to popularize. At the same time, the system also contributes to the management of road vehicles.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

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A MINI PROJECT REPORT

ON

"High Speed Dynamic Strong ARM Latch Comparator"

Submitted in partial fulfilment of the requirements for the award of the degree

BACHELOR OF ENGINEERING IN ELECTRONICS AND COMMUNICATION ENGINEERING

Submitted by

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2022-2023

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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This is to certify that the mini project work entitled "Comparative Analysis of high speed dynamic strong arm latch comparator" is a bonafide work carried out by our student's Ms. Meghana M, USN: 1DB21EC401, Ms.Shashikala B, USN: 1DB21EC406, Ms.Vidyashree R, USN:1DB21EC407 of Sixth semester, in partial fulfilment for the award of degree of Bachelor of Engineering in Electronics and Communication Engineering of Visvesvaraya Technological University, Belagavi in the academic year 2022-2023. It is certified that all corrections/suggestions indicated during mini project work have been incorporated in the report deposited in the department library. The mini project has been approved as it satisfies the academic requirement in respect of the project work described for the partial fulfilment of said degree.

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ABSTRACT

A new design has been proposed for a high speed, low power Strong ARM latch in 65nm CMOS high speed, low power Strong ARM latch in 65nm CMOS technology. Latching speed improvements of 18% and 16% have been achieved in comparison to the conventional and been achieved in comparison to the conventional and improved Strong ARM , respectively, while the energy consumption has also been reduced. As wireless communication is ever-evolving, demanding higher data speeds, the requirements increase for the ADC, and the requirements for the comparator, which is one of the main building blocks, increase as well. The primary purpose of the comparator is to compare two voltage levels and provide a logic output. One significant advantage of dynamic comparators is that they are more power-efficient than traditional comparators. There exist many different architectures for dynamic comparators. In this thesis, the most promising designs are optimized and evaluated over various parameters, such as speed, noise, offset, and hysteresis, while minimizing power consumption.

Keywords—Strong ARM latch, low power circuit design; comparator high speed